

Electronic Acknowledgement Receipt

EFS ID:	5377216
Application Number:	10595157
International Application Number:	
Confirmation Number:	3496
Title of Invention:	Method of Manufacturing Multi-Layer Circuit Board
First Named Inventor/Applicant Name:	Toshiaki Takenaka
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	AttachA_P04030802_RCE.pdf	50078	no	1
			e1be04a79aec86d8f49e1c39f2b48a47a6f86403		

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2	Fee Worksheet (PTO-875)	fee-info.pdf	31911	no	2
			81816e7d489297b6d7849e954cd45b989632f031		

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